

REMARKS

Claims 1-23 are pending in the application.

Claims 12-15 are allowed.

Claims 1, 5-7 and 10 are rejected.

Claims 2-4, 8, 9 and 11 are objected to.

Claims 17-21 were objected to, but indicated as allowable, in the prior office action.

Specification

The specification is amended to correct an informality.

Claim Rejections – 35 U.S.C. § 102

Claims 1, 5-6 and 10 are rejected under 35 U.S.C. § 102(e) as being anticipated by Patel, U.S. 6,396,542 (“Patel”). Applicant traverses this rejection.

Claim 1 recites a television receiver having an *analog* television signal rejection filter to filter one or more spectral components from the frequency spectrum of a received signal. In the example embodiments of Fig. 2 the analog signal rejection filter is shown as item 240, and in the example of Fig. 4, the analog filter is shown as item 440. Thus, the rejection filter appears in the processing path before the analog-to-digital conversion.

Patel does not disclose an analog television signal rejection filter as recited in claim 1. Patel only discloses a *digital* NTSC rejection filter 30 in Fig. 1, which is apparent from its position down-stream of the analog-to-digital converter (ADC) 22. Thus, Patel’s digital rejection filter is a complicated and expensive structure having a delay line and adder (col. 10, lines 11-25) that must respond to the complex digital samples from the real-to-complex sample converter 24 (col. 9, lines 53-59). This is in contrast to the analog rejection filter recited in claim 1, which tends to be simple and relatively inexpensive. For at least this reason, claim 1 is not anticipated by Patel, nor are its associated dependent claims.

New Claim

New claim 24 depends from claim 1 and recites that the DTV received signal processing path includes a demodulator. Claim 24 further recites that the switching circuitry comprises a multiplexer having a first data input connected to the output of the analog television signal

rejection filter, a second data input connected to an output of the demodulator, and a select input to select one of the data inputs as an analog output. Support for these limitations can be found throughout the specification, for example, in Fig. 2 where the multiplexer 260 has a first data input connected to the output of the analog television signal rejection filter 240, a second data input connected to an output of the demodulator 230, and the multiplexer selects one of the data inputs as an analog output.

Patel does not disclose a system as recited in claim 24. In contrast, Patel discloses in Fig. 6 a multiplexer MUX 128 that receives signals provided from conversion filters 126,127 and time compressed NTSC Y,U,V, and outputs a digital signal. Thus, claim 24 is allowable over the prior art.

Allowable Subject Matter

Claims 12-15 are allowed.

Claims 2-4, 8-9 and 11 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form. Claims 2-4, 8-9 and 11 have been rewritten as suggested.

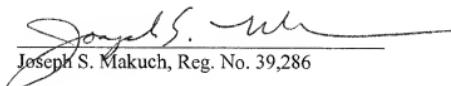
Claims 17-21 were objected to, but indicated as allowable, in the prior office action.

Claims 17-21 have therefore been rewritten as suggested in the prior office action.

Conclusion

Applicant requests reconsideration in view of the foregoing amendments and remarks. The Examiner is encouraged to telephone the undersigned at (503) 222-3613 if it appears that an interview would be helpful in advancing the case.

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